CLAIMS

1. An input control apparatus comprising:

a bit number reduction section that reduces the number of bits of a systematic part and the number of bits of parity parts having a plurality of sequences input to a turbo decoder; and

a control section that controls said bit number reduction section so that the number of bits of one sequence of the parity parts falls below the number of bits of the systematic part.

2. The input control apparatus according to claim 1, wherein said control section controls said bit number reduction section so that the number of bits of the parity parts is obtained in accordance with a coding rate and/or coding block length of a bit sequence input to the turbo decoder.

3. The input control apparatus according to claim 2, wherein said control section performs control so that the number of bits of the parity parts decreases as the coding rate of the bit sequence input to the turbo decoder decreases and the number of bits of the parity parts increases as the coding rate increases.

4. The input control apparatus according to claim 2,

wherein said control section performs control so that the number of bits of the parity parts decreases as the coding block length input to the turbo decoder increases and the number of bits of the parity parts increases as the coding block length decreases.

5. An input control method comprising the step of reducing the number of bits of a systematic part and the number of bits of parity parts so that the number of bits of one sequence of the parity parts falls below the number of bits of the systematic part out of the systematic part and parity parts having a plurality of sequences input to a turbo decoder.